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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/646,718	08/25/2003	Jong-hyoung Lim	2557-000155/US	9851
30593	7590	09/08/2004	EXAMINER	
HARNESS, DICKEY & PIERCE, P.L.C.			HOANG, HUAN	
P.O. BOX 8910			ART UNIT	
RESTON, VA 20195			PAPER NUMBER	
			2818	

DATE MAILED: 09/08/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/646,718	Applicant(s) LIM ET AL.	
	Examiner Huan Hoang	Art Unit 2818	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-28 is/are pending in the application.
 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 20-28 is/are allowed.
- 6) ☒ Claim(s) 1,6,7,11 and 19 is/are rejected.
- 7) ☒ Claim(s) 2-5,12-18 and 8010 is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____. | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Claim Objections

1. Claims 2-5 are objected to because of the following informalities:

The article "a" should be inserted before "first clock signal" (claim 2, line 1), the verb "is" should be inserted before "based" (claim 2, line 1) and the word "controlling" (claim 2, line 5) should be "controls". Appropriate correction is required.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1, 6, 7, 11 and 19 are rejected under 35 U.S.C. 102(b) as being anticipated by Shin.

Shin discloses an integrated circuit memory device and a method of controlling a delay lock loop circuit having all the elements and steps as recited in claims 1, 6, 7, 11 and 19 as follows:

- a delay locked circuit (400, column 2, line 4);
- a DDL power supply (bias generator 420, Fig. 2 and column 2, lines 20-25) supplying power to the DLL circuit;

- a control signal generator (410, Fig. 2) controlling the DLL power supply to selectively supply power to the DLL circuit during a refresh mode (column 1, line 66 to column 2, line 9) of the integrated circuit memory device based on a selection signal (CTRL, Fig. 2); and
- a selection generator (300, Fig. 1) generating the selection signal based on a mode register set command received by the integrated circuit memory device.

The CTRL is generated from the power mode controller 330, which generates the Nap and PDN (Fig. 1).

Allowable Subject Matter

4. Claims 20-28 are allowed.
5. Claims 2-5, 8-10, 12-18 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The prior art does not teach or suggest the following limitations: the control signal generator further controlling the DLL circuit to selectively reset the locking information during the refresh mode of the integrated circuit memory device based on the selection signal, a fuse circuit to generate the selection signal during the refresh mode, , the DLL circuit turned on during a first refresh operation and turned off during a second refresh operation and a control signal generator controlling the DLL circuit to selectively reset the locking information during a refresh mode of the integrated circuit memory device based on a election signal.

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Ryu et al. discloses a semiconductor memory device for providing address access time and data access time at a high speed.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Huan Hoang whose telephone number is (571) 272-1779. The examiner can normally be reached on Mon-Fri 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C Nelms can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Huan Hoang
Primary Examiner
Art Unit 2818